

Notic of R ferenc s Cited	Application/Control No. 09/229,592	Applicant(s)/Patent Under Reexamination DOYLE ET AL.	
	Examiner Paul E Brock II	Art Unit 2815	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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	U	Wolf, Silicon Processing for the VLSI ERA, vol. 2: Process Integration, 1990, ppgs. 144 -150
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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